

Attorney Docket No. 5646-102

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In: Yew-Keong Chong et al.

Group Art Unit: 2819

Serial No.: 10/616,272

Examiner: To Be Assigned

Filed: July 9, 2003

Confirmation No.: 1710

For: IMPEDANCE-MATCHED OUTPUT DRIVER CIRCUITS HAVING LINEAR  
CHARACTERISTICS AND ENHANCED COARSE AND FINE TUNING CONTROL

Date: December 17, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

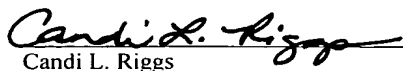
Respectfully submitted,

  
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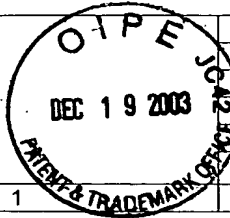
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 17, 2003.

  
Candi L. Riggs

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1



Complete if Known

Application Number	10/616,272
Filing Date	July 9, 2003
First Named Inventor	Yew-Keong Chong et al.
Group Art Unit	2819
Examiner Name	To Be Assigned
Attorney Docket Number	5646-102

**U.S. PATENTS AND PATENT PUBLICATIONS**

Examiner Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code (if known)		
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	2.	6,604,227		Saito	05-2000
	3.	6,570,405	B1	Lien	05-27-2003
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	32.	4,731,553		Van Lehn et al.	03-1988

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T
		Office	Number	Kind Code (if known)			

**OTHER NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
	33.	Fan et al., "On-Die Termination Resistors with Analog Impedance Control for Standard CMOS Technology," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003	